

Universal Microwave Components Corp.

5702-D General Washington Drive Alexandria, VA 22312 <> Web: www.umcc111.com E-mail: sales@umcc111.com <> Tel: (703) 642-6332 <> Fax: (703) 642-2568

Model: SR-J410-16S

Description: SP16T, Absorptive, PIN-Switch
Operating Frequency: 0.5 – 14 GHz
Insertion Loss: 4.9 dB Max
Isolation (Arm-to-Arm):

0.5 – 12.4 GHz	12.4 –14 GHz
60 dB Min	55 dB Min

VSWR ("ON" paths & "OFF" ports):		1.9:1 Max							
Rise/Fall time (10% to 90% RF, 90% to 10									
On/Off time (50% TTL to 90% RF, 50%TT	L to 10% RF):	90 ns Max							
Operating Power:	CW & AVG	+27dBm Max *							
		(+24dBm Max into "JX" Port @ Off-State)							
Control Input Characteristics:		TTL / Unit-Load / Floating-High							
Control Logic:									
Power Supplies:		+5(±0.25)V @ 430mA Max							
• •		, ,							
Connectors (RF):		, ,							
Connectors (Supplies/Controls)		Solder Pins							
Impedance:		50 Ω Nominal							
Quality:		Best-Commercial-Grade							
Environmental Ratings:									
Temperature:	{Operating: -55°C to +95°C} & {Stor	age: -60°C to +110°C}							
Humidity:	MIL-STD-202F, Method 103B, Cond	d. B (96 hours at 95% R.H.)							
Shock:									
Vibration:									
Altitude:		,							
Temp. Shock:	MIL-STD-202F. Method 107D. Cond	G. A (5 cycles)							

Available Options:

(Units with listed options here may be subject to some specification tradeoffs from the standard, consult factory)

■ Supply Voltages

A1 [+5(±0.25)V / -5(±0.25)V]* **A2** [+5(±0.25)V / -15(±0.5)V]

■ RF Connectors

B1 [J1 – J16 Ports with SMA(M)] **B2** [All SMA (M)]

B3 [COM Port with SMA(M)]

■ Control Function

F1 [Inverse Logic, ("1" = Path "ON")]

F2 [4-Bit Binary Controls + Enable]; see below for

"Outline Drawing" and "Function Table"

■ Video Suppression Filter

G1 [VF @ COM Port <> 0.25 dB additional Loss]

G2 [VF @ J1 – J16 Ports <> 0.25 dB additional Loss]

G3 [VF @ All Ports <> 0.5 dB additional Loss]

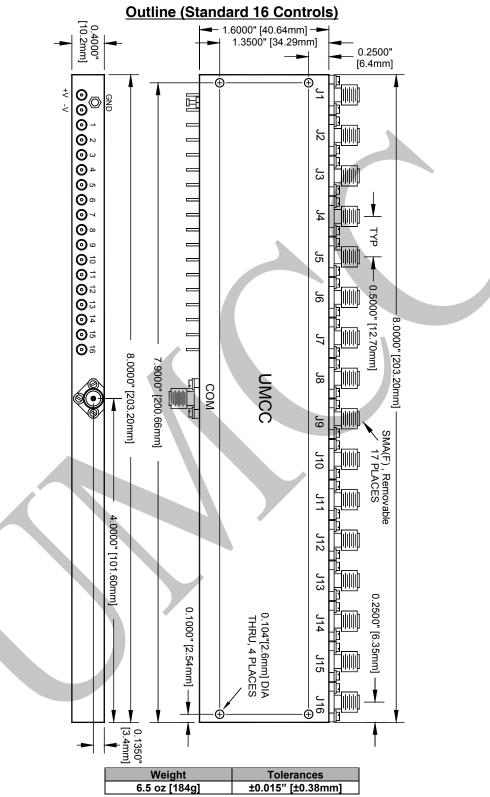
^{*} Recommended for Low Power Consumption <> Additional Switching Delay (ON/OFF time)





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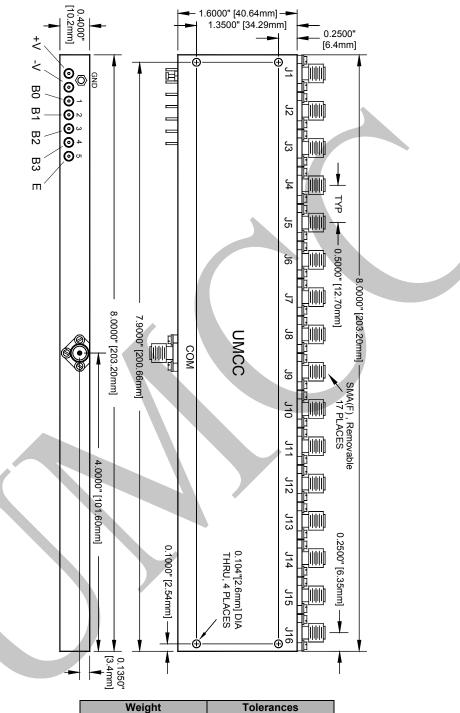




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Outline (Option "F2" for 4-Bit Binary Control + Enable)





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Function Table for Option "F2" (4-Bit Binary Control + Enable)

	II	NPUT	3		RF PATH FROM "COM" PORT TO "JXX" PORT															
Е	В3	B2	B1	В0	J1	J2	J3	J4	J5	J6	J7	J8	J9	J10	J11	J12	J13	J14	J15	J16
0	0	0	0	0	ON	OFF														
0	0	0	0	1	OFF	ON	OFF													
0	0	0	1	0	OFF	OFF	ON	OFF												
0	0	0	1	1	OFF	OFF	OFF	ON	OFF											
0	0	1	0	0	OFF	OFF	OFF	OFF	ON	OFF										
0	0	1	0	1	OFF	OFF	OFF	OFF	OFF	ON	OFF									
0	0	1	1	0	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF								
0	0	1	1	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF							
0	1	0	0	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF						
0	1	0	0	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF
0	1	0	1	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF
0	1	0	1	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF
0	1	1	0	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF
0	1	1	0	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF
0	1	1	1	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF
0	1	1	1	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON
1	Х	Χ	Х	Х	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF

