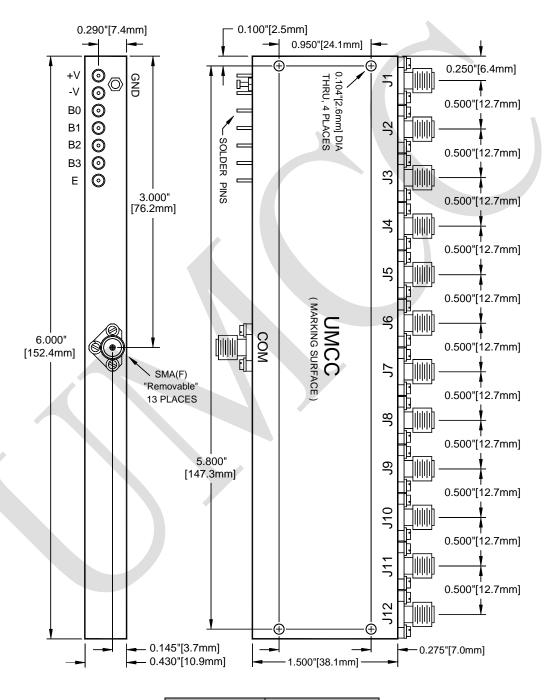


SR-XXXX-12S-F2 (Option with 4-Bit Binary Decoder)

Outline Drawing (SP12T with Option "F2")

Fig. 3



Weight	Tolerances						
	±0.015" [±0.38mm]						



SR-XXXX-12S-F2 (Option with 4-Bit Binary Decoder)

Function Table for Option "F2" (4-Bit Binary Control + Enable)

INPUTS					RF PATH FROM "COM" PORT TO "JXX" PORT											
E	В3	B2	B1	B0	J1	J2	J3	J4	J5	J6	J7	J8	J9	J10	J11	J12
0	0	0	0	0	ON	OFF										
0	0	0	0	1	OFF	ON	OFF									
0	0	0	1	0	OFF	OFF	ON	OFF								
0	0	0	1	1	OFF	OFF	OFF	ON	OFF							
0	0	1	0	0	OFF	OFF	OFF	OFF	ON	OFF						
0	0	1	0	1	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF
0	0	1	1	0	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF
0	0	1	1	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF
0	1	0	0	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF
0	1	0	0	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF
0	1	0	1	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF
0	1	0	1	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON
1	Х	Х	Х	Х	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF