



**Model: SR-DA3W-12S**

Description:.....	SP12T, Reflective, PIN-Switch
Operating Frequency:.....	0.2 – 4 GHz
Insertion Loss:.....	2.4 dB Max
Isolation:.....	65 dB Min
VSWR (“ON” paths):.....	1.7:1 Max
Rise/Fall time (10% to 90% RF, 90% to 10% RF):... ..	350 ns Max
On/Off time (50% TTL to 90% RF, 50%TTL to 10% RF):.....	400 ns Max
Operating Power:.....	CW&AVG..... +27dBm Max
.....	Peak:..... 5W @ 1µS PW & 10% Duty-Cycle Max
Control Input Characteristics:.....	Std. TTL / Unit-Load / Floating-High
Control Logic:.....	12-Controls, Logic “0” = RF Path “ON”
Power Supplies:.....	+5(±0.25)V @ 130mA Max
.....	-12(±0.5)V @ 150mA Max
Connectors (RF):.....	SMA (f), Removable
Connectors (Supplies/Controls).....	Solder Pins
Impedance:.....	50 Ohms Nominal
Quality:.....	Best-Commercial-Grade

**Environmental Ratings:**

Temperature:.....	{Operating: -55°C to +95°C} & {Storage: -60°C to +110°C}
Humidity:.....	MIL-STD-202F, Method 103B, Cond. B (96 hours at 95% R.H.)
Shock:.....	MIL-STD-202F, Method 213B, Cond. B (75G, 6mSec)
Vibration:.....	MIL-STD-202F, Method 204D, Cond. B (.06” double amplitude, or 15G)
Altitude:.....	MIL-STD-202F, Method 105C, Cond. B (50,000 Feet)
Temp. Shock:.....	MIL-STD-202F, Method 107D, Cond. A (5 cycles)

**Available Options:**

(Units with listed options here may be subject to some specification tradeoffs from the standard, consult factory)

■ **Supply Voltages**

- A1 [ +5(±0.25)V / -5(±0.25)V ]
- A2 [ +5(±0.25)V / -15(±0.5)V ]
- A3 [ +12(±0.5)V / -12(±0.5)V ]
- A4 [ +15(±0.5)V / -15(±0.5)V ]

■ **RF Connectors**

- B1 [ J1 – J12 Ports with SMA(M) ]
- B2 [ All SMA (M) ]
- B3 [ COM Port with SMA(M) ]

■ **RF Connectors**

- E1 [ 15-Pin Micro-D Male (Plug) with Jack-posts ]  
“see Fig.2 in UMCC Web for Outline Drawing”

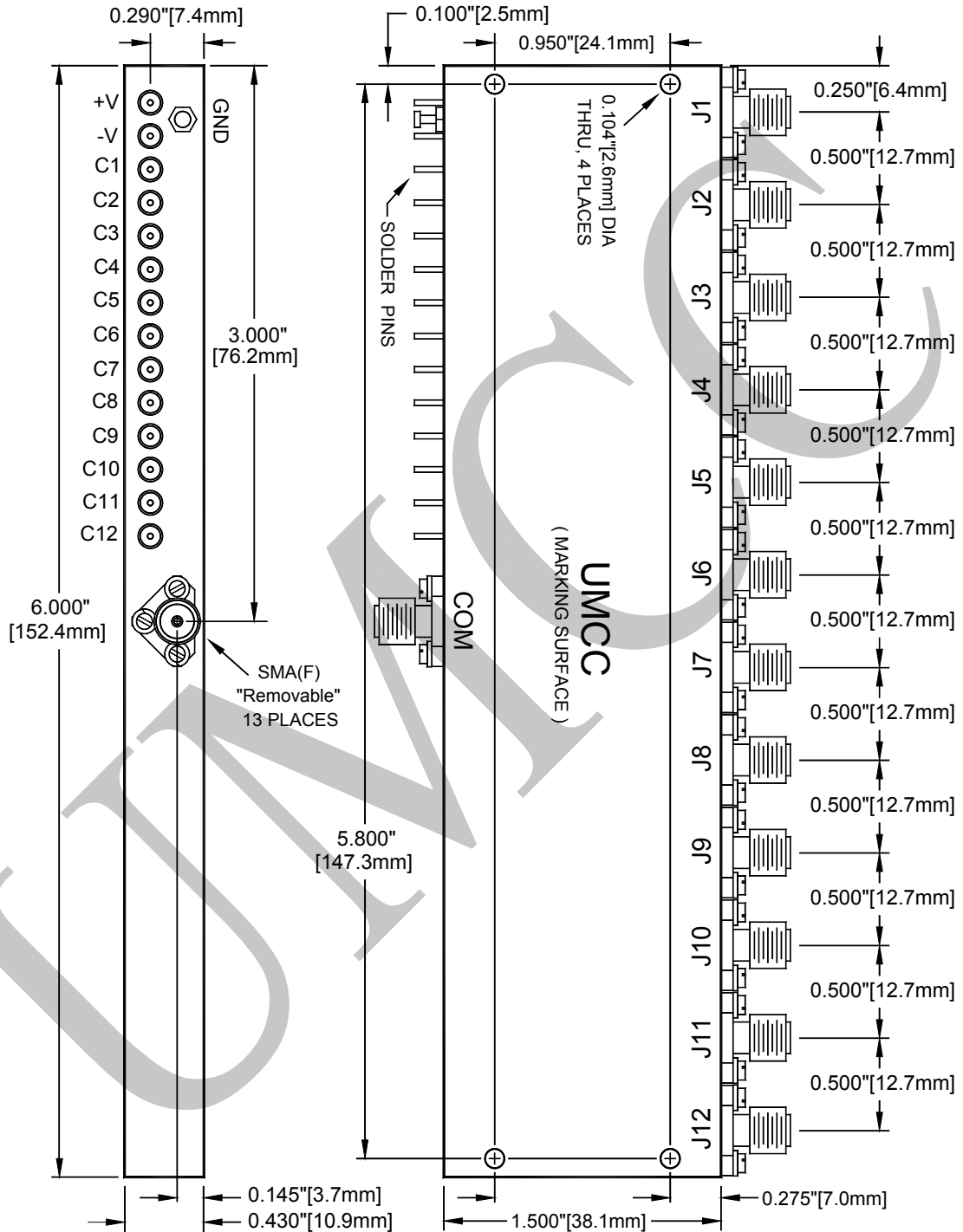
■ **Control Function**

- F1 [ Inverse Logic, (“1” = Path “ON”) ]
- F2 [ 4-Bit Binary Controls + Enable ] ; see below for  
“Outline Drawing” and “Function Table”



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Outline



Weight	Tolerances
	±0.015" [±0.38mm]





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Function Table for Option "F2" (4-Bit Binary Control + Enable)

INPUTS					RF PATH FROM "COM" PORT TO "JXX" PORT											
E	B3	B2	B1	B0	J1	J2	J3	J4	J5	J6	J7	J8	J9	J10	J11	J12
0	0	0	0	0	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF
0	0	0	0	1	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF
0	0	0	1	0	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF
0	0	0	1	1	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF
0	0	1	0	0	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF
0	0	1	0	1	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF
0	0	1	1	0	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF	OFF
0	0	1	1	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF	OFF
0	1	0	0	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF	OFF
0	1	0	0	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF	OFF
0	1	0	1	0	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON	OFF
0	1	0	1	1	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	ON
1	X	X	X	X	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF